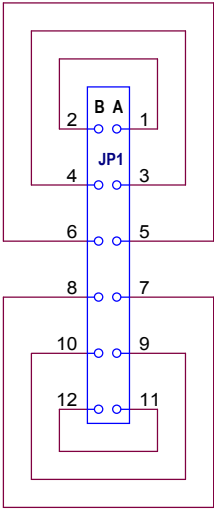


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-		PRODUCTION FAB	BJS	5 - 22 - 15



WURTH, 613 012 218 21

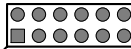
CUSTOMER NOTICE LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		APPROVALS PCB DES. NC APP ENG. BJS <	
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REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	1	PRODUCTION	BJS	6-30-15

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS
LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER.
MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT
TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. PLUG JP1 INTO DC2343A-PCB1 AS SHOWN TO COMPLETE THE DC2343A-ASSY 1
NOTE: PCB BOARD IS MOUNTED ON TOP OF THE JP1 HEADER

JP1, 2x6, SOCKET
WURTH, 61301221821



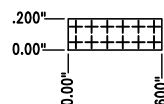
DC2343A-PCB1
TOP MOUNTED

APPROVALS				1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.Linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
PCB DES.	NC				
APP ENG.	BJS	TITLE: TOP ASSEMBLY DRAWING:		PCB FOR DC2343A-1-ASSY1	
		SIZE	IC NO.	REV.	
		N/A	DC2343A-PCB1	1	
SCALE = NONE		FILENAME: DC2343A-PCB1.PCB		SHT 1 of 2	

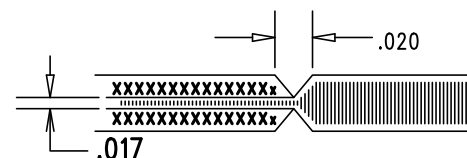
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	1	PRODUCTION	BJS	6-30-15

NOTES: UNLESS OTHERWISE SPECIFIED

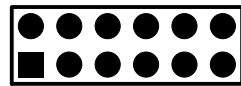
- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
 - FINISHED THICKNESS TO BE 0.062" +/- .005"
 - TOTAL OF 2 LAYERS WITH 1 OZ. CU ON PRIMARY AND SECONDARY
 - FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
 - ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 - HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
 - GOLD IMMERSION BOTH SIDES.
 - (LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
 - FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB: "PRODUCTION FAB ONLY"



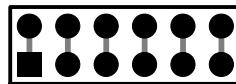
SIZE	QTY	SYM	PLATED	TOL
0.04134	12	+	YES	+/-0.003



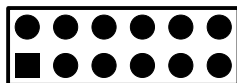
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ±1 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994	APPROVALS		 LINEAR TECHNOLOGY	1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.Linear.com LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY	
	PCB DES.	NC			
	APP ENG.	BJS			
	TITLE: FABRICATION DRAWING: PCB FOR DC2343A-1-ASSY1				
THIRD ANGLE PROJECTION			SIZE N/A	IC NO. DC2343A-PCB1	REV. 1
					
DO NOT SCALE DRAWING	SCALE: NONE		FILENAME: DC2343A-PCB1.PCB		SHT 1 of 1



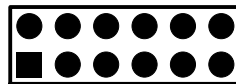
TOP SOLDER MASK
LINEAR TECHNOLOGY
DC2343A-PCB1
6-30-15



LAYER 1 – TOP SIDE
LINEAR TECHNOLOGY
DC2343A-PCB1
6-30-15



LAYER 2 – SECONDARY SIDE
LINEAR TECHNOLOGY
DC2343A-PCB1
6-30-15



BOTTOM SOLDER MASK
LINEAR TECHNOLOGY
DC2343A-PCB1
6-30-15